

Form PTO-1449 (modified)

List of Patents and Publications

For Applicant's Information

Disclosure Statement

(Use several sheets if necessary)

ATTY. DKT. NO. 5580-03100

APPLICANT: Rogenmoser, et al.

FILING DATE: July 9, 2001

SERIAL NO. 09/901,740

GROUP: Unknown

U.S. PATENT DOCUMENTS

EXAM. INITIALS	REF. DES.	DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS	FILING DATE IF APPROPRIATE

FOREIGN PATENT DOCUMENTS

EXAM. INITIALS	REF. DES.	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB CLASS	TRANSLATION YES/NO

OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)

TDC	A1	SiByte, "Target Applications," http://sibyte.com/mercurian/applications.htm , January 15, 2001, 2 pages.
TDC	A2	SiByte, "SiByte Technology," http://sibyte.com/mercurian/technology.htm , January 15, 2001, 3 pages.
TDC	A3	SiByte, "The Mercurian Processor," http://sibyte.com/mercurian , January 15, 2001, 2 pages.
TDC	A4	SiByte, "Fact Sheet," SB-1 CPU, October 2000, rev. 0.1, 1 page.
TDC	A5	SiByte, "Fact Sheet," SB-1250, October 2000, rev. 0.2, 10 pages.
TDC	A6	Stepanian, SiByte, "SiByte SB-1 MIPS64 CPU Core, Embedded Processor Forum 2000, June 13, 2000, 15 pages.
TDC	A7	Jim Keller, "The Mercurian Processor: A High Performance, Power-Efficient CMP for Networking," October 10, 2000, 22 pages.
TDC	A8	Weste, et al., "Principles of CMOS VLSI Design," A Systems Perspective, Addison-Wesley Publishing Company, June 1988, 5 pages.

EXAMINER:

T. Cunningham

DATE CONSIDERED:

4/25/03

EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to the patent owner.

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U.S. PATENT DOCUMENTS

SEP 21 2001

EXAM. INITIALS	REF. DES.	DOCUMENT NUMBER	DATE	NAME	CLASS & TRADEMARK OFFICE	SUB CLASS	FILING DATE IF APPROPRIATE

FOREIGN PATENT DOCUMENTS

EXAM. INITIALS	REF. DES.	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB CLASS	TRANSLATION YES/NO

OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)

TDC	A9	Tom R. Halfhill, "SiByte Reveals 64-Bit Core For NPUs; Independent MIPS64 Design Combines Low Power, High Performance," Microdesign Resources, June 2000, Microprocessor Report, 4 pages.

EXAMINER: T. Cunningham

DATE CONSIDERED:

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 PATENT & TRADEMARKS

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U.S. PATENT DOCUMENTS

EXAM. INITIALS	REF. DES.	DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS	FILING DATE IF APPROPRIATE
TDC	B1	5,789,966	8/4/98	Bechade	357	407	—
TDC	B2	6,066,978	5/23/00	Kadowaki	327	407	—
TDC	B3	5,646,558	7/8/97	Jamshidi	327	408	—

FOREIGN PATENT DOCUMENTS

EXAM. INITIALS	REF. DES.	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB CLASS	TRANSLATION YES/NO

OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)

TDC	B4	International Search Report for EP Application No. 02 015 278.1, mailed October 28, 2002.					

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